



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of )  
Shunpei YAMAZAKI et al. )  
Serial No. 09/916,484 )  
Filed: July 30, 2001 )  
For: ELECTRIC DEVICE, MATRIX )  
DEVICE, ELECTRO-OPTICAL DISPLAY )  
DEVICE, AND SEMICONDUCTOR )  
MEMORY HAVING THIN-FILM )  
TRANSISTORS )

Art Unit: 2823

Examiner: S. Foong

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with  
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*[Signature]*

**SUBMISSION OF CORRECTED FORMAL DRAWINGS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the *Notice of Allowability*, enclosed please find 1 sheet of  
corrected Formal Drawing consisting of Figures 1A-1B and 2A-2B.

Respectfully submitted,

*[Signature]*  
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